

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:	)	Group Art Unit: 2826
Yasuhiko TAKEMURA	)	Examiner: Ahmed N. Sefer
Serial No. 09/342,235	)	CERTIFICATE OF MAILING  I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on December 19, 2006.
Filed: June 29, 1999	)	
For: SEMICONDUCTOR DEVICE HAVING	)	
AT LEAST FIRST AND SECOND	)	
THIN FILM TRANSISTORS	)	adde M Stamper
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## **RESPONSE**

Honorable Commissioner of Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The Official Action mailed June 19, 2006, has been received and its contents carefully noted. Filed concurrently herewith is a *Request for Three Month Extension of Time*, which extends the shortened statutory period for response to December 19, 2006. Also, filed concurrently herewith is a *Notice of Appeal*. Accordingly, the Applicant respectfully submits that this response is being timely filed.

The Applicant notes with appreciation the consideration of the Information Disclosure Statements filed on June 29, 1999; October 25, 2000; October 31, 2000; June 4, 2001; November 30, 2001; March 7, 2002; May 7, 2002; November 8, 2002; February 24, 2003; July 13, 2004; March 9, 2005; June 1, 2005; and November 30, 2005.

Claims 6-11 are pending in the present application, of which claims 6 and 9-11 are independent. For the reasons set forth in detail below, all claims are believed to be in condition for allowance. Favorable reconsideration is requested.

Paragraphs 3 and 4 of the Official Action reject claims 6-11 as obvious based on the combination of U.S. Patent No. 5,905,555 to Yamazaki and JP 63-076474 to